eMMC-NAND RECONSTRUCTOR

Alexander (Sasha) Sheremetov - Rusolut

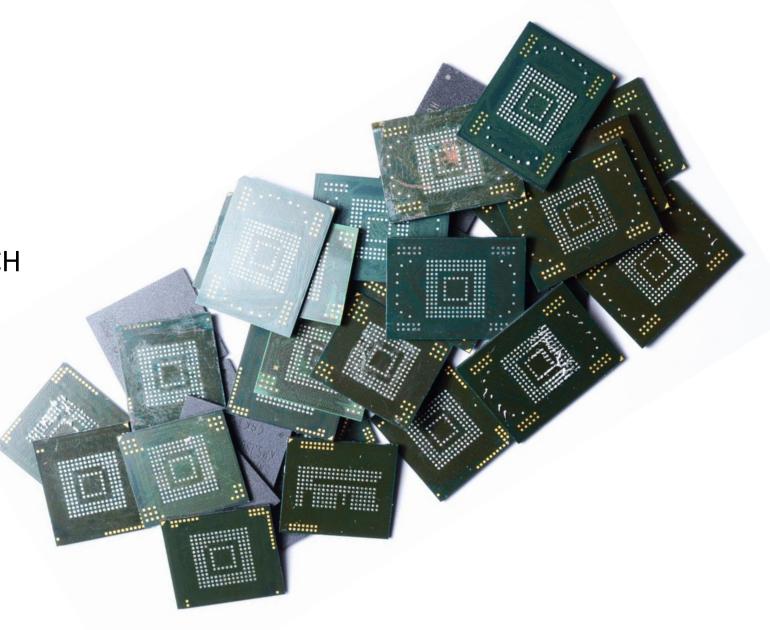


APPLICATIONS OF EMMC CHIPS IS WIDE

- SMARTPHONES
- TABLETS
- DRONES
- CARS
- SATNAV SYSTEMS
- WEARABLES/SMARTWATCH
- LAPTOPS
- VOICE RECORDERS
- MULTIMEDIA PLAYERS
- TV BOXES
- SMART TV
- INTERNET OF THINGS

...AND MUCH MORE...





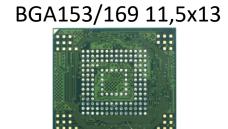
COMMON EMMC/EMCP CHIPS USED IN PHONES AND OTHER DEVICES



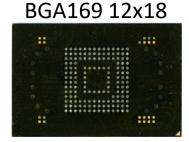








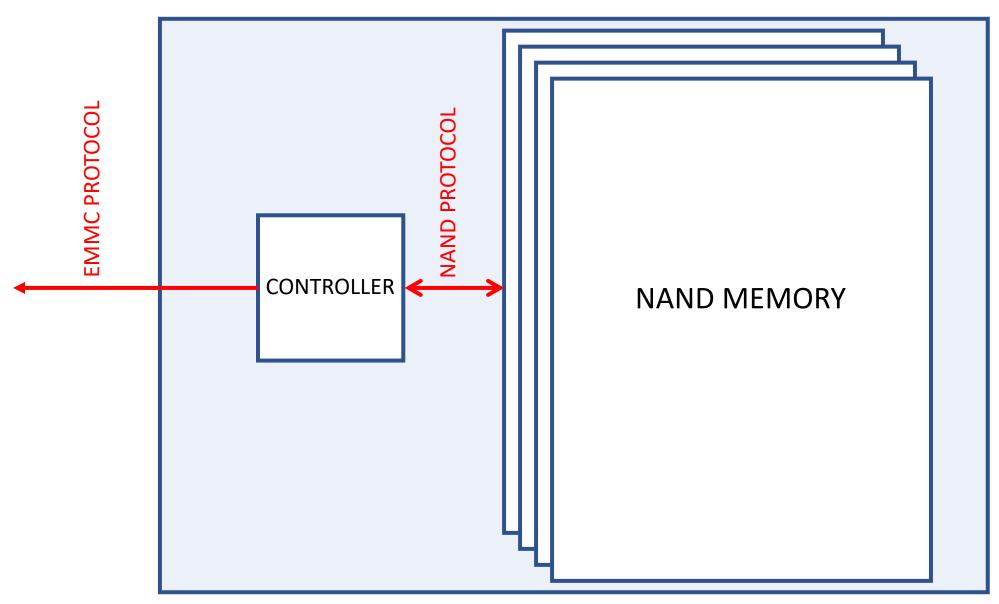






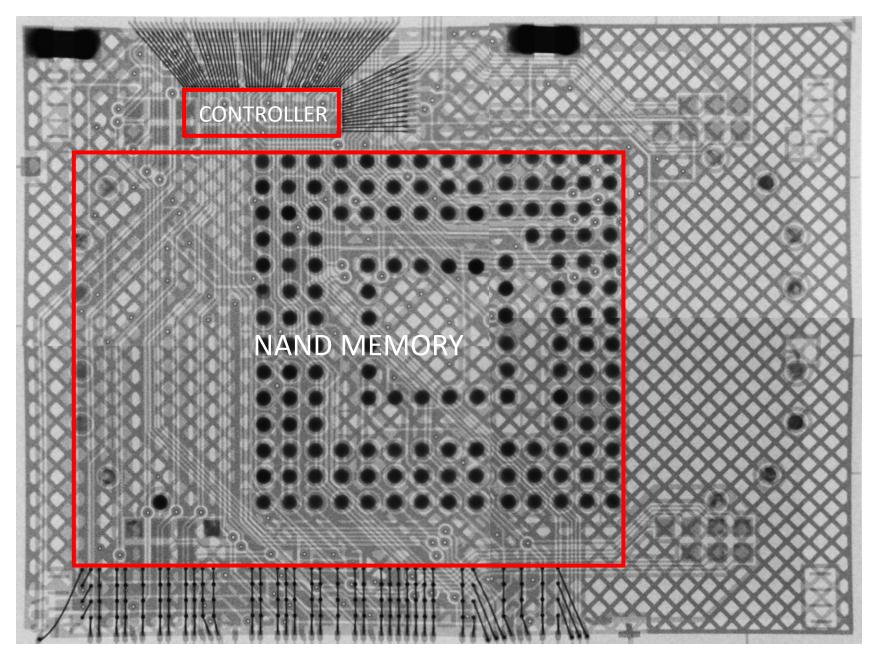


INSIDE EMMC



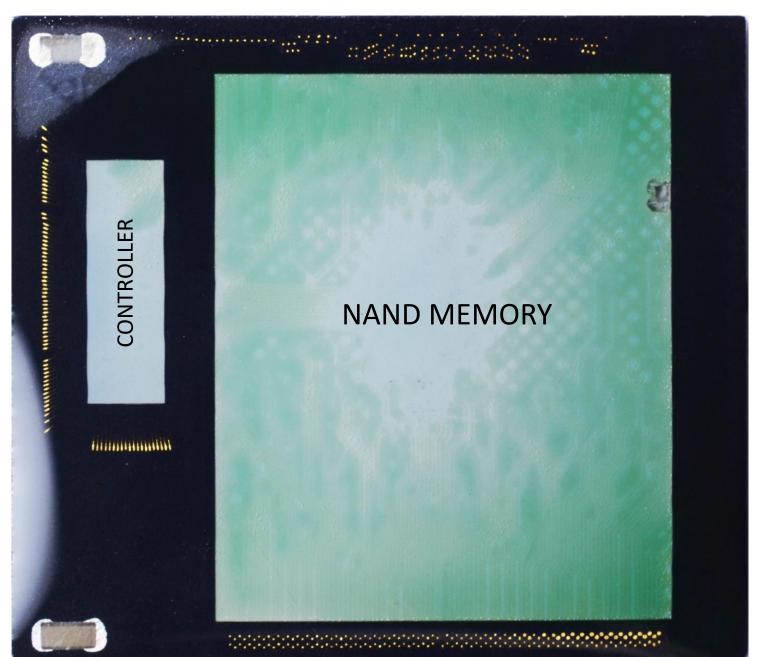


EMMC THROUGH XRAY

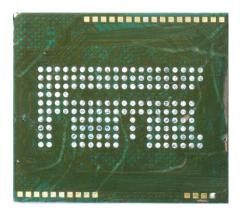


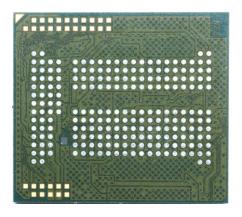


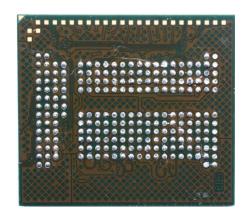
EMMC - CHIP LAYER

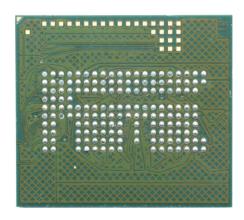




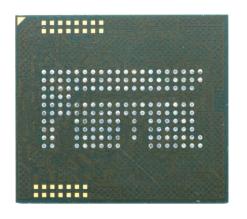


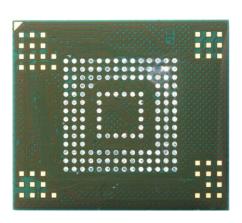


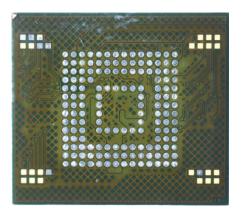


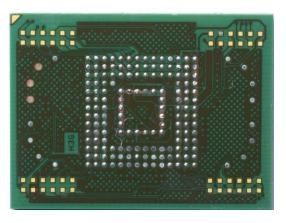


TECHNOLOGICAL PADS - NAND INTERFACE











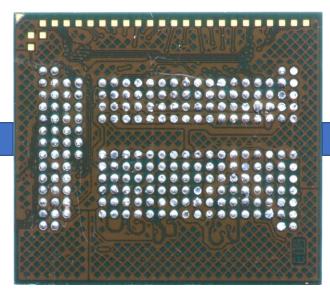
WHY CARE ABOUT GETTING PHYSICAL IMAGE THROUGH NAND?

QUICK EXTRACTION

WORKING CHIPS

LOGICAL IMAGE (95-97% OF DATA)

eMMC

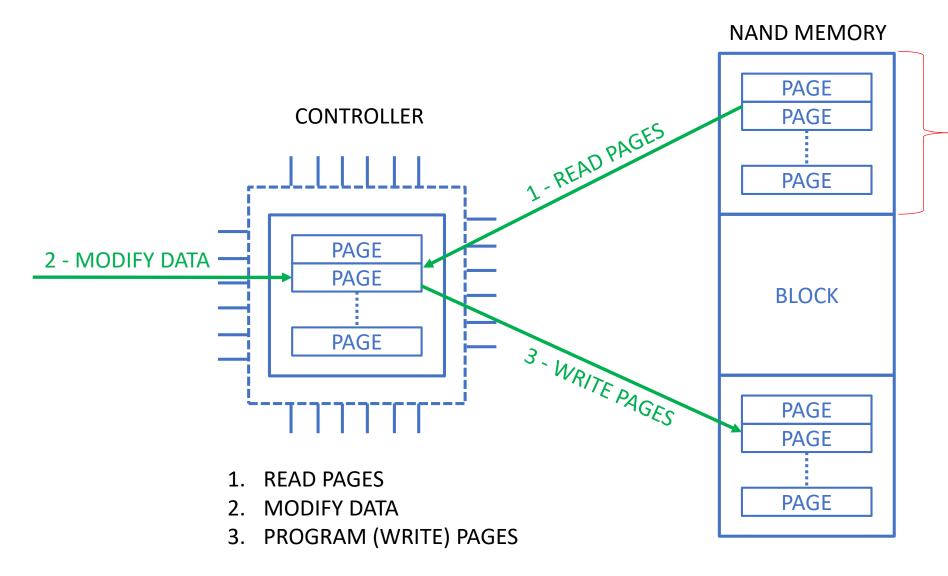


NAND

- DEEP EXTRACTION OF DELETED DATA
- DATA EXTRACTION EVEN AFTER RESET
- DEAD CHIPS
- TRUE PHYSICAL IMAGE (100% OF DATA)



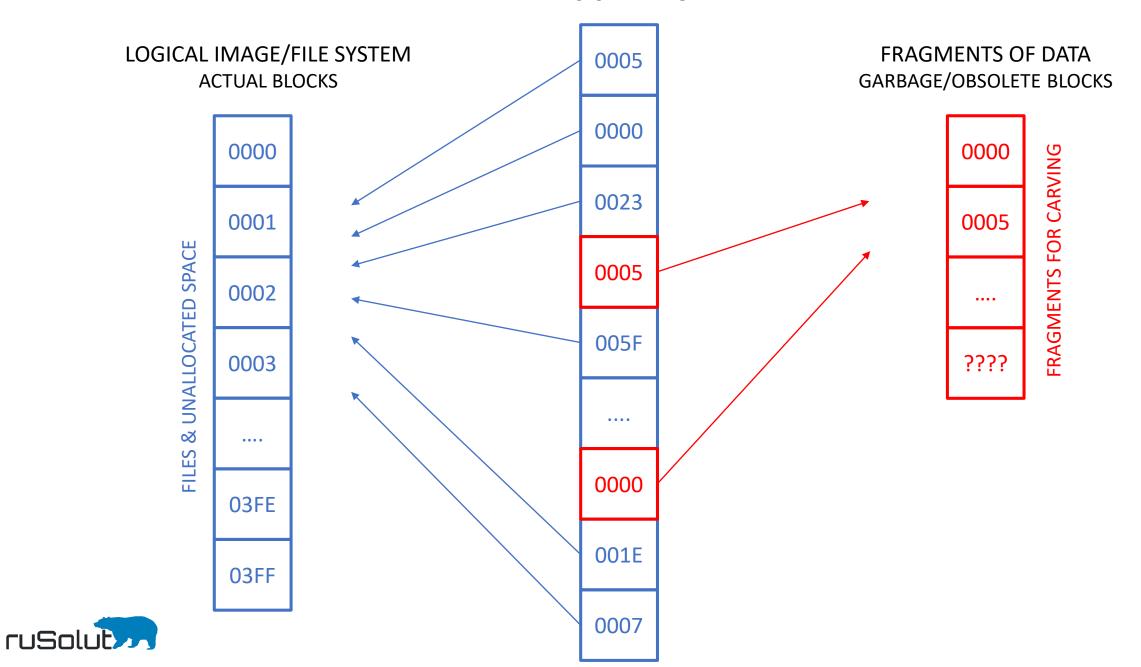
DATA RECOVERY FROM GARBAGE BLOCKS OF NAND



OLD UNERASED BLOCK STAYS UNTOUCHED FOR SOME TIME UNTIL GARBAGE COLLECTION ALGORITHM ERASE IT. USUALLY IT'S NOT FAST PROCESS



PHYSICAL IMAGE



HOW MANY GARBAGE BLOCKS CAN WE FIND IN AVERAGE DUMP?

FROM SEVERAL BLOCKS TO SEVERAL DOZENS



WHAT CAN/CANNOT BE CARVED FROM THE FRAGMENTS, REALISTICALLY

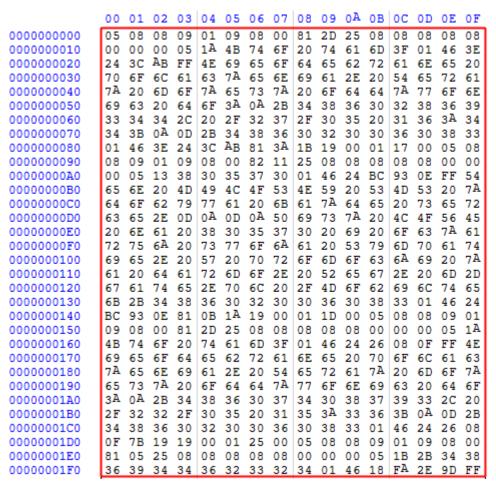


ruSolut

8KB FRAGMENT OF DATA...IS IT A LOT? HOW MUCH EXACTLY?



512 BYTES OF DATA FROM OBSOLETE PAGE



```
.....Kto tam?.F>
$< wsNieodebrane
polaczenia. Tera
z Mozesz oddzwon
ic do:.+4860286
, /27/05 16:4
4;..+4860200
.F>$<«Γ:.....
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WITHIN 512 BYTES OF DATA WE CAN SEE 3 SMS.

PAGE IS AT LEAST 16 TIMES LARGER.

SIMPLE CALCULATION SHOWS THAT ONE PAGE MAY CONTAIN ROUGHLY 45-50 SMS (OR CHAT MESSAGES).

NOW ASSUME THAT ONE BLOCK IS AT LEAST 128 PAGES.

 $128 \times 45 = 5760.$

SO ONE BLOCK MAY CONTAIN ~ 5000 MESSAGES.

ON PRACTISE YOU'LL GET LOTS OF DUPLICATED RECORDS DUE TO THE NATURE OF SQLITE.

SO LET'S JUST SHIRINK IT DOWN TENFOLD TO 500. IT IS STILL A HUGE AMOUNT OF DATA!!!



IS THERE ANY LIFE AFTER FACTORY RESET?

YOU NEVER KNOW UNTIL YOU CHECK IT BUT AT LEAST GARBAGE BLOCKS ARE RARELY ERASED



DAMAGED EMMC CHIPS

CAUSES

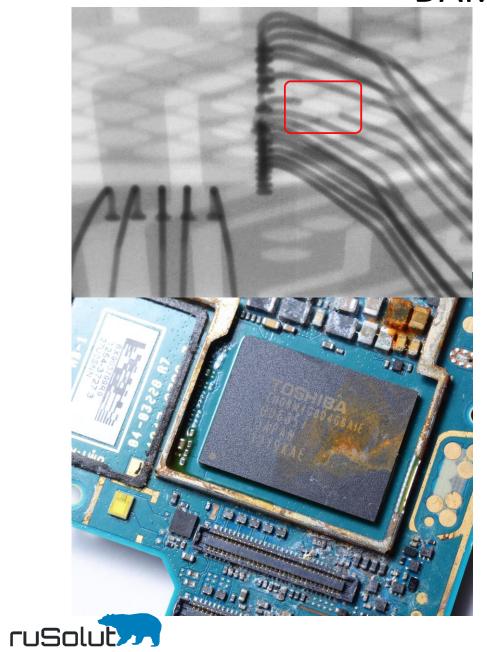
- WATER DAMAGE
- THERMAL DAMAGE
- PHYSICAL DAMAGE
- DAMAGE OF TRACKS/PADS ON CHIP'S PCB
- DAMAGE OF WIRE BONDING INSIDE CHIP
- HUMAN FACTOR DURING DATA RECOVERY

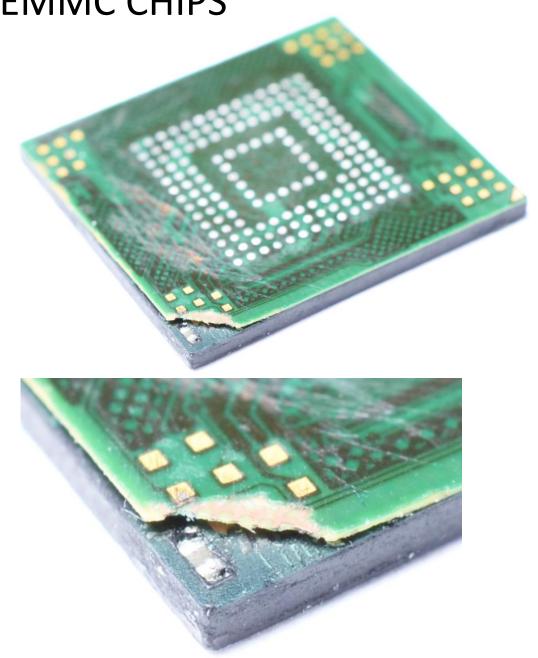
SYMPTOMS

- NOT RECOGNIZED WHEN CONNECTED TO EMMC ADAPTER
- RECOGNIZED BUT SHOWS WEIRD CAPACITY
- RECOGNIZED AND FIRST 32-64MB ACCESSIBLE
- RECOGNIZED BUT READS GARBAGE



DAMAGED EMMC CHIPS





SCENARIOS OF FAILURE

NO SHORT CIRCUIT ~80-90%

- FW CORRUPTION
- CONTROLLER DAMAGE DUE TO OVERHEAT
- WIRE BONDING DAMAGE
- UNKNOWN COTROLLER DAMAGES

SHORT CIRCUIT ~10-20%

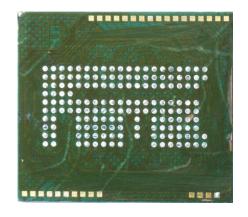


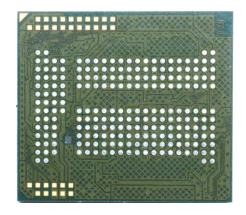
PROBLEMS WE HAD TO SOLVE TO TRANSFORM TECHNOLOGY INTO A TOOL

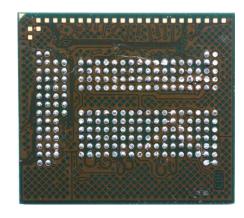
- UNKNOWN TECHNOLOGICAL NAND PINOUTS
- CONNECTING TO CHIPS
- NAND CONFIGURATIONS (EASY ONE)
- READ RETRY (DON'T MISS THIS PRESENTATION TODAY!)
- ADAPTIVE SCRAMBLING SCHEMES (NIGHTMARE)
- SCRAMBLED DA+SA+ECC (DON'T MISS THIS PRESENTATION TODAY!)
- PAGE BASED TRANSLATION ALGORITHMS ☺
- DEVELOPMENT OF SPECIAL SQLITE CARVER & FILE CARVER (DON'T MISS THESE PRESENTATIONS TODAY!)

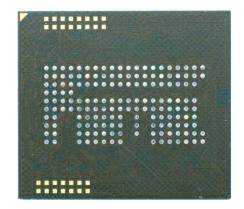


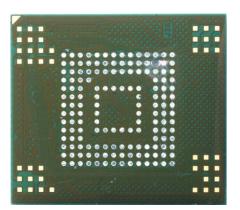
DIVERSITY OF DEVICES AND TECHNOLOGICAL PINOUTS

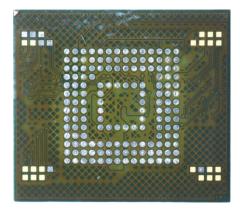












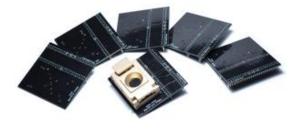


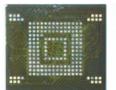
eMMC NAND RECONSTRUCTOR - HARDWARE

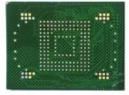




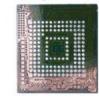
eMMC NAND RECONSTRUCTOR - ADAPTERS



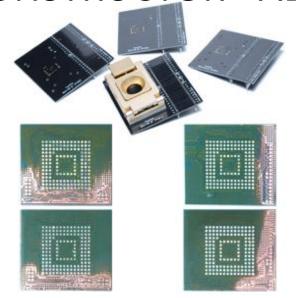




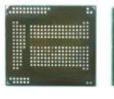




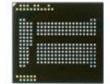




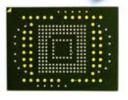




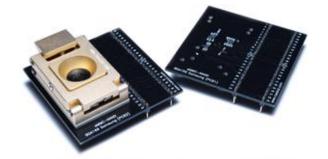




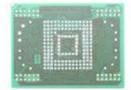


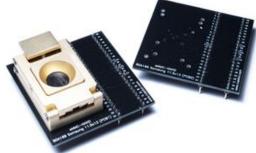






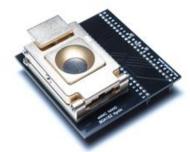








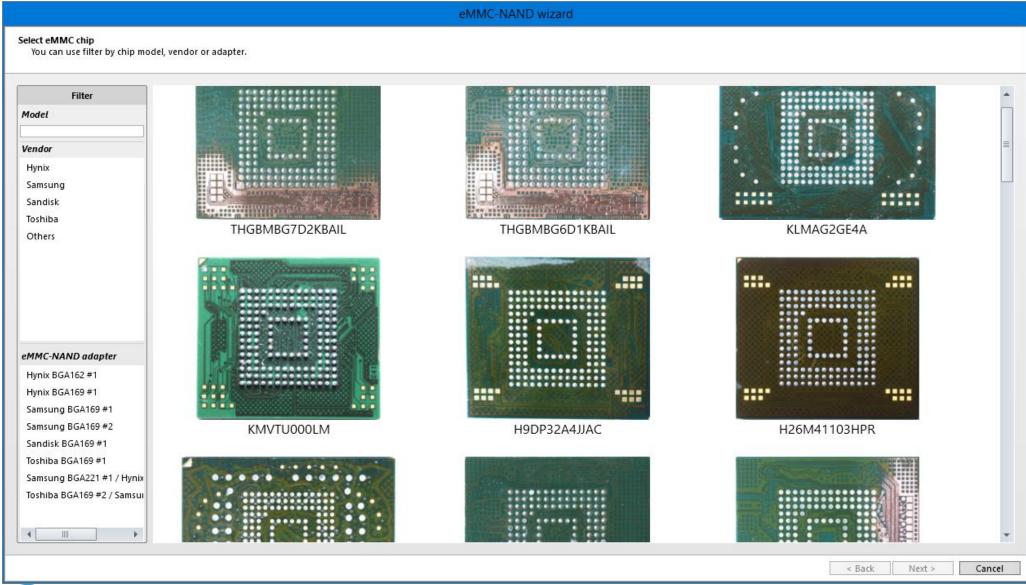






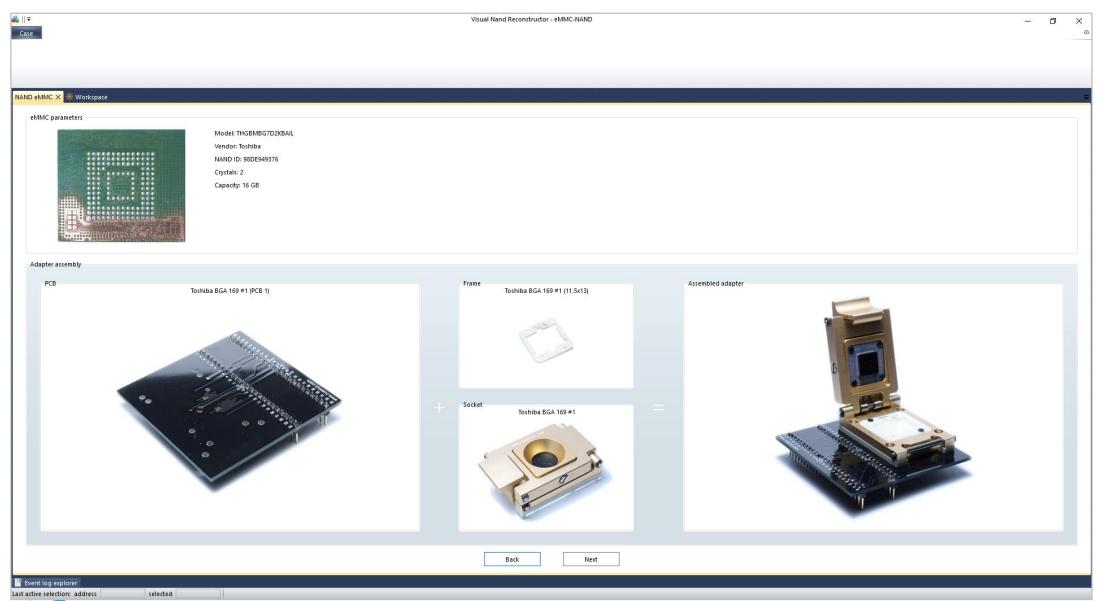


eMMC NAND RECONSTRUCTOR - SOFTWARE



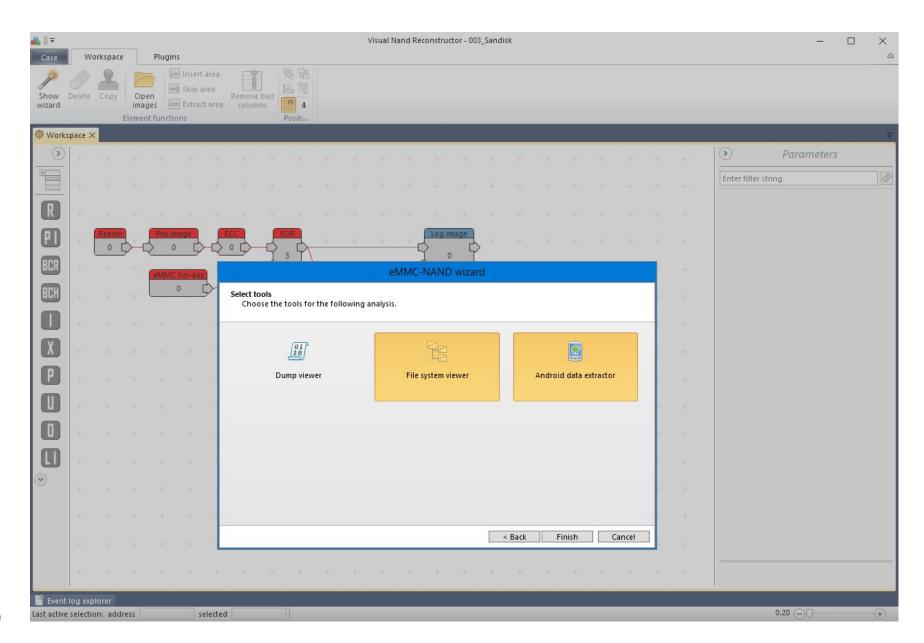


eMMC NAND RECONSTRUCTOR - SOFTWARE





AUTOMATIC CONTROLLER RECONSTRUCTION





NOW LET'S TAKE A SHORT COFFEE BREAK AND MOVE FORWARD TO PRACTICAL PART

WE HAVE A QUICK 15-MIN WORKSHOP AREAS WHERE YOU CAN HAVE SOME CHAT WITH OUR ENGINEERS!

